

## Abstract of Disclosure

An apparatus for generating a wobble clock includes an operation circuit for receiving a wobble signal and counting cycles of an oscillating signal during a cycle of the  
5 wobble signal to derive a cycle value and for comparing the cycle value with a preset cycle value to output a first protection signal, and a phase-locked loop circuit connected to the operation circuit for receiving the wobble signal, the wobble clock and the first protection signal, and determining whether the wobble clock is to be synchronized with the wobble signal according to the first protection signal.